

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

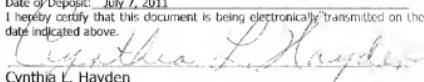
In re Applicant:	§	Art Unit:	2482
Louis Lippincott et al.			
Serial No.:	10/750,075	§	Examiner: Chikaodili E. Anyikire
Filed:	December 31, 2003	§	Conf. No.: 2083
For:	Motion Estimation Sum of all Differences (SAD) Array Having Reduced Semiconductor Die Area Consumption	§	Docket: ITL.1703US P17498
		§	Assignee: Intel Corporation

Mail Stop **RCE**
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

PRELIMINARY AMENDMENT

Sir:

Prior to the receipt of an office action, please amend the above-referenced patent application as follows:

Date of Deposit: July 7, 2011
I hereby certify that this document is being electronically transmitted on the date indicated above.

Cynthia L. Hayden